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Inventor: Barrie Gilbert

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Title: SQUARING CELLS AND MULTIPLIERS USING SUMMED EXPONENTIALS

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)U.S. PATENT DOCUMENTS

<u>Exam Init</u>	<u>Ref</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub- Class</u>
<u>M</u>	_____	6,052,349	04/2000	Okamoto	369	47.26
<u>M</u>	_____	5,714,911	02/03/1998	Gilbert	331	57
<u>M</u>	_____	5,684,431	11/04/1997	Gilbert et al.	330	254
<u>M</u>	_____	5,298,811	03/29/94	Gilbert	327	357
<u>M</u>	_____	5,432,478	07/11/1995	Gilbert	330	284
<u>M</u>	_____	5,077,541	12/31/1991	Gilbert	330	284
<u>M</u>	_____	5,107,150	04/21/1992	Kimura	327	349
<u>M</u>	_____	4,047,235	09/1977	Davis	361	100

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	<u>Document Number</u>	<u>Publication Date</u>	<u>Country</u>	<u>Name</u>
<u>M</u>	EP 0 665 639 AI	08/95	Europe	Gilbert

OTHER DOCUMENTS

<u>Exam Init</u>	<u>Ref</u>	<u>(Including Author, Title, Date, Pertinent Pages, Etc.)</u>
<u>M</u>	_____	Gilbert, Barrie; Wideband Negative-Current Mirror; <i>Electronics Letters</i> ; Vol. 11, No. 6, March 20, 1975; pages 126-127
<u>M</u>	_____	Gilbert, Barrie; <i>Current-mode Circuits From A Translinear Viewpoint: A Tutorial</i> ; Analogue IC design: the current mode approach; Peter Peregrinus, Ltd., London, United Kingdom; 1990 (pages 11-91)

Examiner: nowDate Considered: 11/8/04